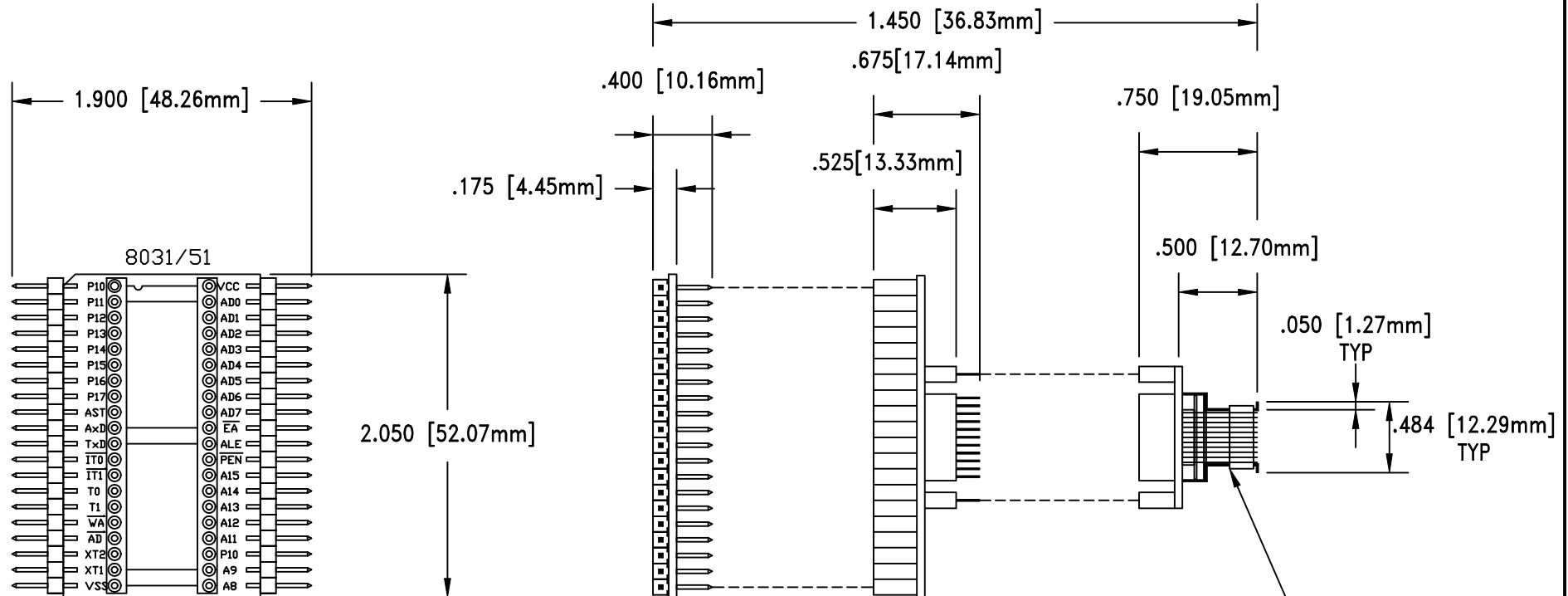


REV	DESCRIPTION	DATE	BY	F5184
A	NEW DRAWING	11/06/00	M.D.	
B	ADDED PIN MAP	09/04/01	H.N.	DOD 35184



DIP PIN	TQFP PIN	FUNCTION	DIP PIN	TQFP PIN	FUNCTION
1	40	P1.0	21	18	P2.0/A8
2	41	P1.1	22	19	P2.1/A9
3	42	P1.2	23	20	P2.2/A10
4	43	P1.3	24	21	P2.3/A11
5	44	P1.4	25	22	P2.4/A12
6	1	P1.5	26	23	P2.5/A13
7	2	P1.6	27	24	P2.6/A14
8	3	P1.7	28	25	P2.7/A15
9	4	RST	29	26	PSEN
10	5	RxD/P3.0	30	27	ALE
	6	NC		28	NC
11	7	TxD/P3.1	31	29	EA
12	8	INT0/P3.2	32	30	P0.7/AD7
13	9	INT1/P3.3	33	31	P0.6/AD6
14	10	TO/P3.4	34	32	P0.5/AD5
15	11	T1/P3.5	35	33	P0.4/AD4
16	12	WR/P3.6	36	34	P0.3/AD3
17	13	AD/P3.7	37	35	P0.2/AD2
18	14	XTAL2	38	36	P0.1/AD1
19	15	XTAL1	39	37	P0.0/AD0
20	16,17,39	GND	40	38	Vcc

ALL DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED

Emulation Technology, Inc.
 — VLSI and SMT ADAPTERS and ACCESSORIES —

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 TEL:(408)982-0660 FAX:(408)982-0664

NO.	ITEM	DESCRIPTION	QTY.
1	AX-8031/51-SUB	TOP SUB ASSEMBLY	1
2	DIP-QF16S-8051	MIDDLE SUB ASSEMBLY	1
3	EPP044QF16SM	BOTTOM SUB ASSEMBLY	1

SHEET: 1 OF 1 DATE: 09/04/01 REVISION: B

CHECKED: Perry Munroe DRAWN: Miguel Delgadillo

ASSEMBLY DRAWING

ITEM: AS-V-163DS

DO NOT SCALE DRAWING DESCRIPTION: AS-DIP-QF16DS-8031/51