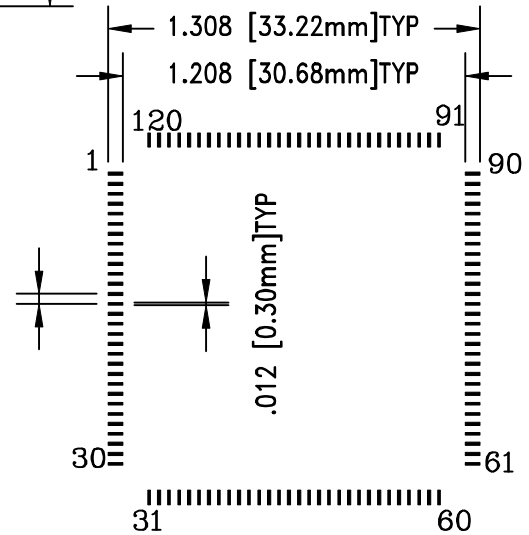


RECOMMENDED
MINIMUM PAD LAYOUT



PIN MAP

1	120	119	117	114	111	108	105	102	99	96	93	91
3	2	118	116	113	110	107	104	101	98	95	92	90
6	5	4	115	112	109	106	103	100	97	94	88	89
9	8	7								85	86	87
12	11	10								82	83	84
15	14	13								79	80	81
18	17	16								76	77	78
21	20	19								73	74	75
24	23	22								70	71	72
27	26	25								67	68	69
29	28	34	37	40	43	46	49	52	55	64	65	66
30	32	35	38	41	44	47	50	53	56	58	62	63
31	33	36	39	42	45	48	51	54	57	59	60	61

SCALE 2:1

NO.	ITEM	DESCRIPTION	QTY.
1	120-QF05-S	BASE BOARD	1
2	PINS2	MOUNT ON FRONT OF #1	120
3	QF05-SPACER-A	SOLDER TO REAR OF #1	1
4	SPACER-2BG016	USE WITH #2	1



Emulation Technology, Inc.
— VLSI and SMT ADAPTERS and ACCESSORIES —

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SHEET: 1 OF 1	DATE: 11/7/00	REVISION:	ASSEMBLY DRAWING
CHECKED: Paula Olson	DRAWN: Aaron Fine	ITEM: EPP120QF05SM	
DO NOT SCALE DRAWING			DESCRIPTION: EPP-120-QF05-SM